



Missing EDA Links

SoC GDS

Analyze, Convert & Verify
Standard Layout Databases

DOLPHIN INTEGRATION

SoC GDS offers an intuitive user interface providing advanced productivity enhancing functionalities throughout the design creation and validation chains.

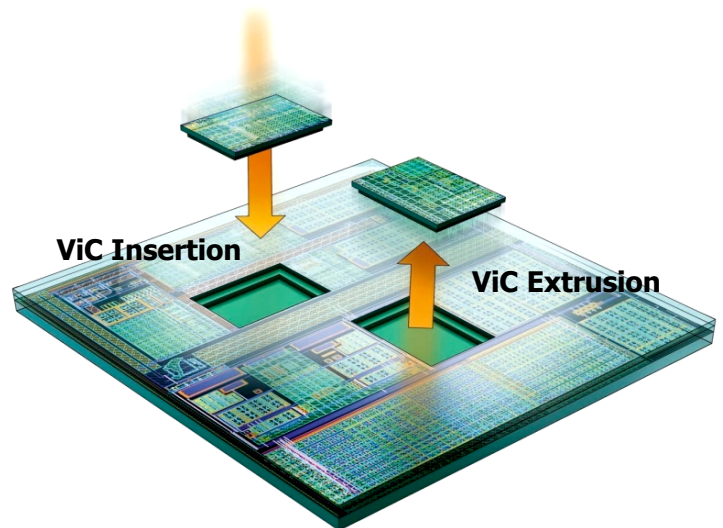
SoC GDS addresses a wide range of needs, from quick and easy layout viewing, to final insertions of cells before mask generation, via advanced hierarchical integration of blocks, including solutions for preserving confidentiality in case of verifications.

This framework independent Streamer focuses on standard exchange formats for bridging proprietary EDA Frameworks.

Through dedicated options, SoC GDS fits the needs of Virtual Component providers, SoC integrators and process/product engineers. It is also the ideal solution to complete quality procedures for acceptance of layout databases by chip finishing teams, mask shops or silicon foundries.

KEY FEATURES

- ✓ Integrated hierarchy browser
- ✓ GDSII, OpenAccess, OASIS and LEF formats
- ✓ Ultra-fast layout loading and display
- ✓ Multi-platform GUI and CLI
- ✓ Interoperable with frameworks
- ✓ Graphical and geometrical comparison
- ✓ Virtual Socket Builder for generation of black boxes enabling verification and integration
- ✓ Virtual Cut for layout extraction
- ✓ Batch processing mode for automated verification and integration



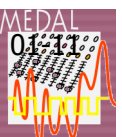
OPTIONS FOR A WIDE RANGE OF APPLICATIONS

- **SoC GDS Analyzer** is the entry-level option which provides GDSII display capabilities. This includes color and pattern configuration file compatibility with Cadence to be easily used as a substitute or complement in design flows.
- **SoC GDS Babelizer** extends the speed and power of SoC GDS Analyzer with advanced hierarchical navigation, input format support complemented with OpenAccess, OASIS and LEF, as well as (VC)LEF generation. Verification of layouts is accelerated through hierarchical graphic comparison of cells and hierarchical display of annotated nets.
- **SoC GDS Binder** enables socket creation and verification for system-level integration of blocks and ViCs (Virtual Components). Delivery of layouts is accelerated by creation of LVS sockets (black box), hierarchical geometrical comparison of cells and a powerful interpreted scripting language for repetitive processing. An innovative feature for Virtual Cut allows to extract just a part of a layout for verification while totally preserving confidentiality of the rest of the SoC.



SoC GDS is available identically under Linux and Windows.

dolphin-integration.com/eda
gds2@dolphin-integration.com





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Options for a Wide Range of Applications

DOLPHIN INTEGRATION

		Discovery	Analyzer	Babelizer	Binder
Basics	Load and display multiple uncompressed GDSII libraries	●	●	●	●
	Display hierarchy in integrated browser	●	●	●	●
	Tolerance on minor GDSII format errors	●	●	●	●
	Log stream-in and stream-out layer mapping	●	●	●	●
Input / Output	Load and display multiple compressed GDSII, OpenAccess, OASIS libraries		●	●	●
	Filtered cell list for fast access to large GDSII, OpenAccess, OASIS libraries		●	●	●
	Save or copy images to file/clipboard		●	●	●
	Cadence display configuration compatibility interface		●	●	●
Analyzing	Save preconfigured technology & display configurations		●	●	●
	Graphically configure layer display attributes and visibility		●	●	●
	Adjustable hierarchical depth expansion in layout window		●	●	●
	Navigate hierarchy through hierarchy browser		●	●	●
	Configurable rendering speed and precision		●	●	●
Exploring	Set and display marks and measures, XOR, DRC		●	●	●
	Printing (all platforms) & Plotting (Windows)		○	○	○
	Navigate hierarchy through instance highlighting			●	●
	Get information on polygons and subcells			●	●
	Geometric shape filtering/searching with object explorer			●	●
	Interactive net highlighting			●	●
	Port and net explorer in abstract views			●	●
	Display of Calibre DB, Hercules and layout error markers			●	●
	Calibre RVE integration			●	●
	Formats	OASIS (Open Artwork System Interchange Standard) load and display			●
LEF (Layout Exchange Format) load and display				●	●
OA (OpenAccess) database load and display				●	●
Convert GDSII to (VC)LEF				●	●
Comparison	Configurable GDSII identification card generation			●	●
	Hierarchical graphic cell (GDSII, OASIS, OA or LEF) comparison			●	●
	Hierarchical geometric cell (GDSII, OASIS, OA or LEF) comparison				●
	Graphic abstract (LEF) comparison				●
Export	Geometric LEF versus GDSII comparison				●
	GDSII stream-out				●
	OASIS stream-out				●
	ASCII export				●
	OpenAccess database save				●
	Remap cell and layer names				●
	Flatten hierarchy				●
Batch	Create LVS sockets for hierarchical black-box LVS verification				●
	ViC Insertion and Extrusion				●
	Selected layout exchange with Virtual Cut				●
Batch	Integrated batch scripting interface for GDSII processing				●
	Integrated Tcl interface for automatic checking of deliverables				●
	Integrated Tcl interface for processing very big files				●
	Integrated Tcl interface for generating complex assembly				●

Features marked ○ are available as an add-on to the standard option.

Discover the complete set of features of each option and register for the free download of SoC GDS Discovery on our web site at: http://www.dolphin-integration.com/medal/socgds/socgds_download.html

SoC GDS is available identically under Linux and Windows.

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gds2@dolphin-integration.com

